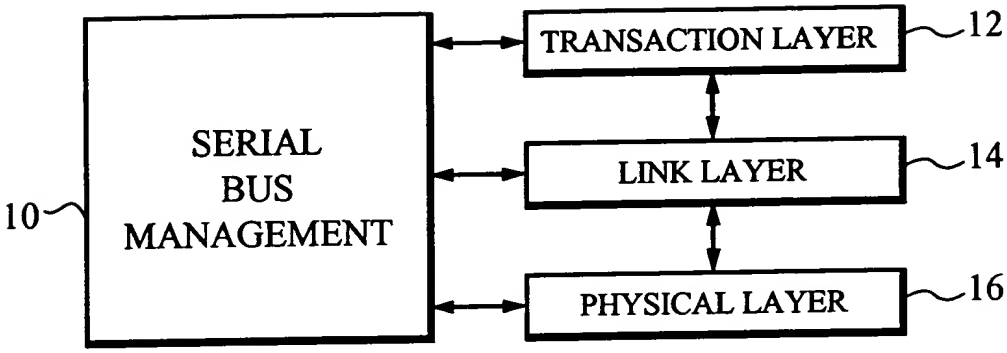

Fig. 1 (PRIOR ART)

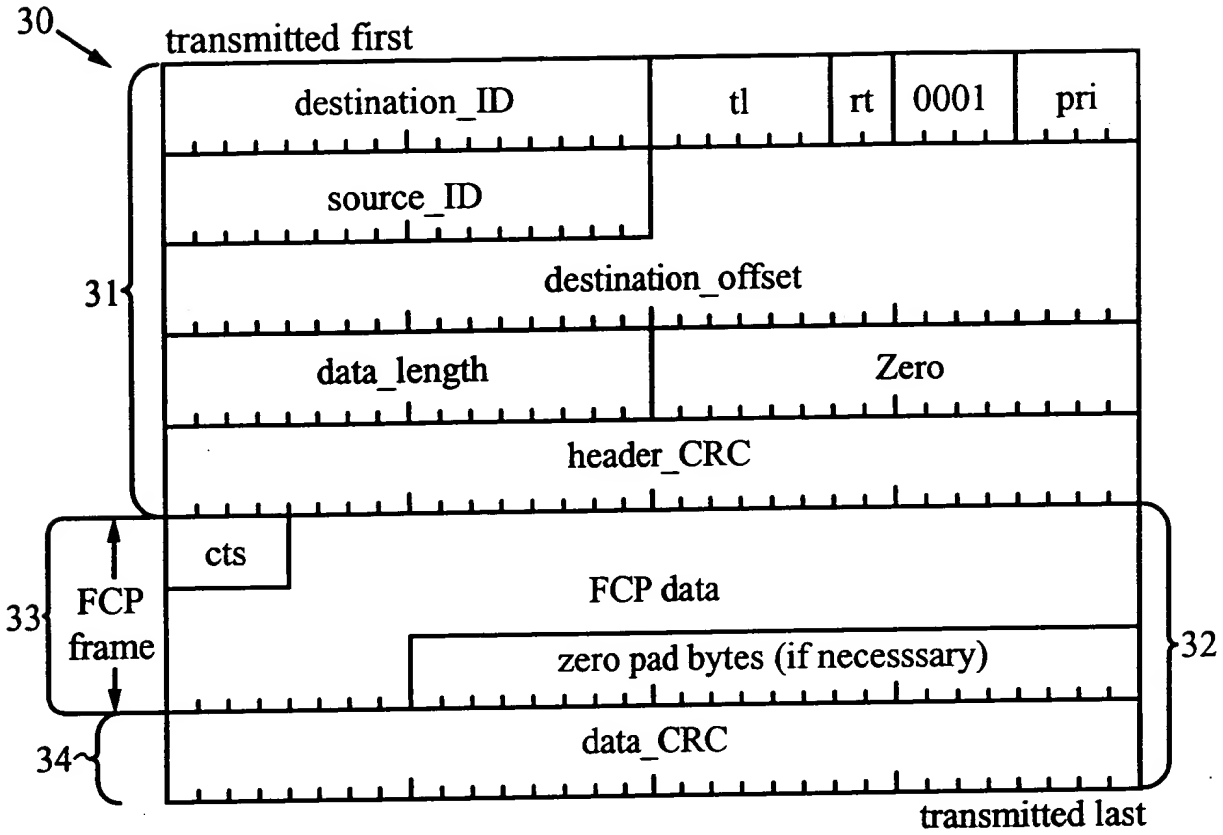

Fig. 2 PRIOR ART

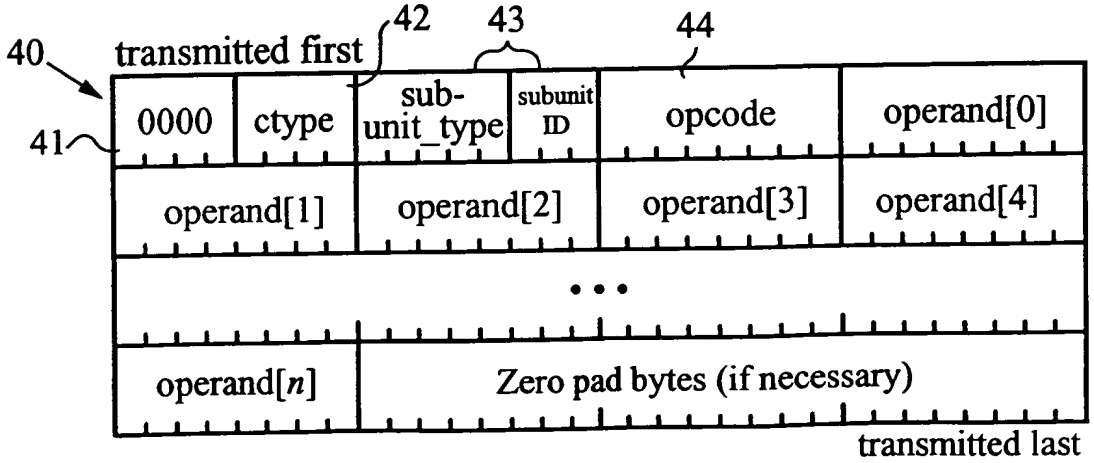

Fig. 3A PRIOR ART

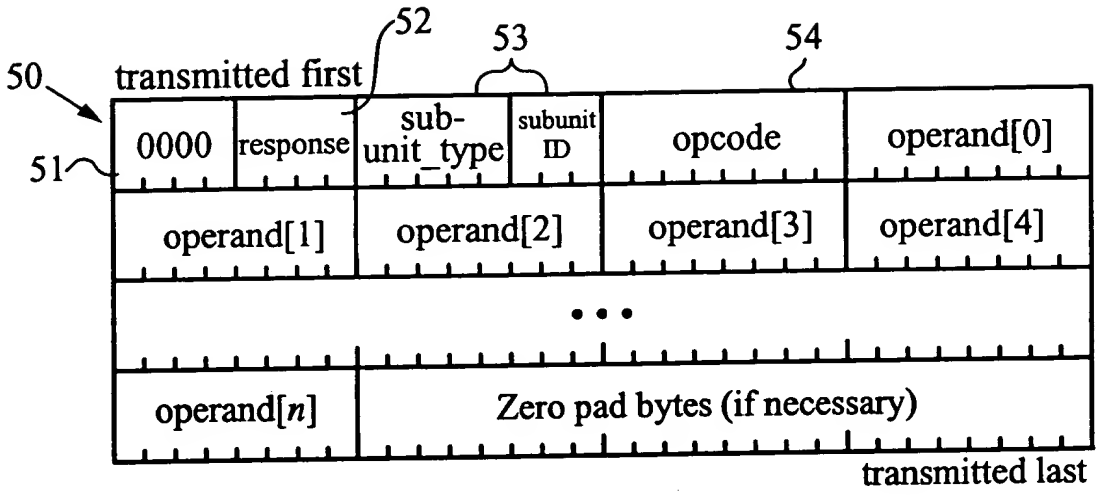

Fig. 3B *PRIOR ART*

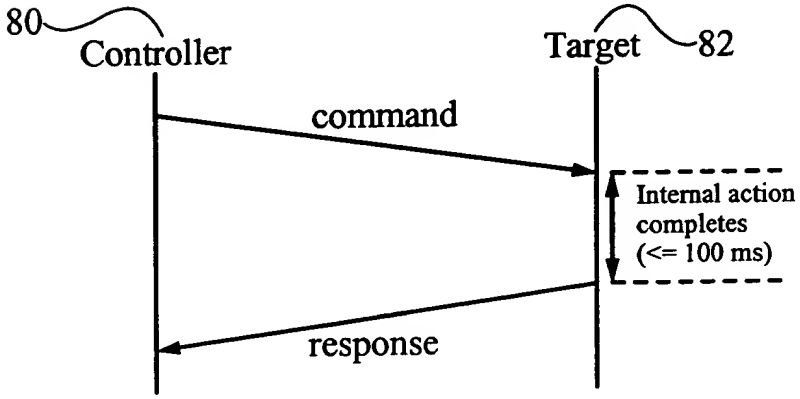

Fig. 4

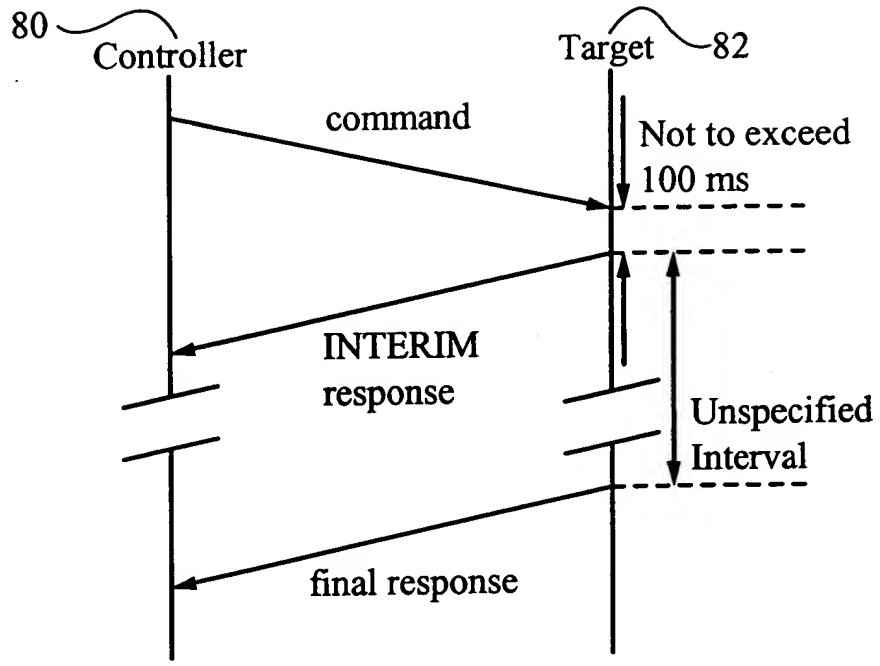
80
Controller
Target
82
command
Not to exceed
100 ms
INTERIM
response
Unspecified
Interval
final response

Fig. 5

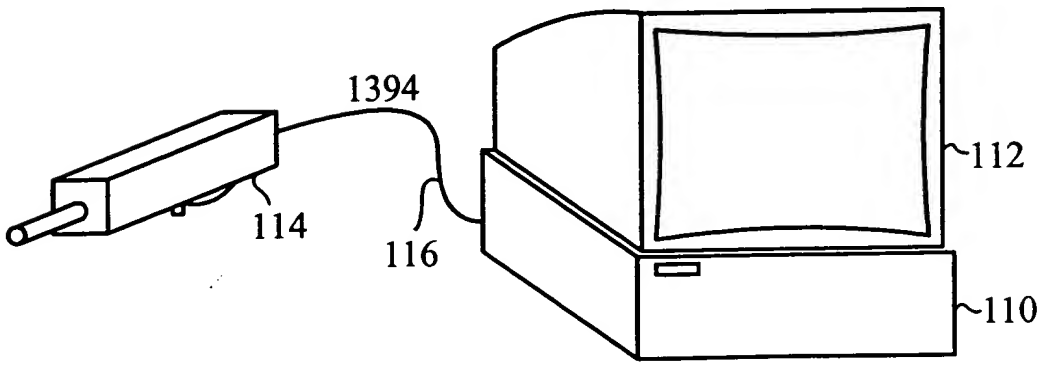
1394
112
114
116
110

Fig. 6

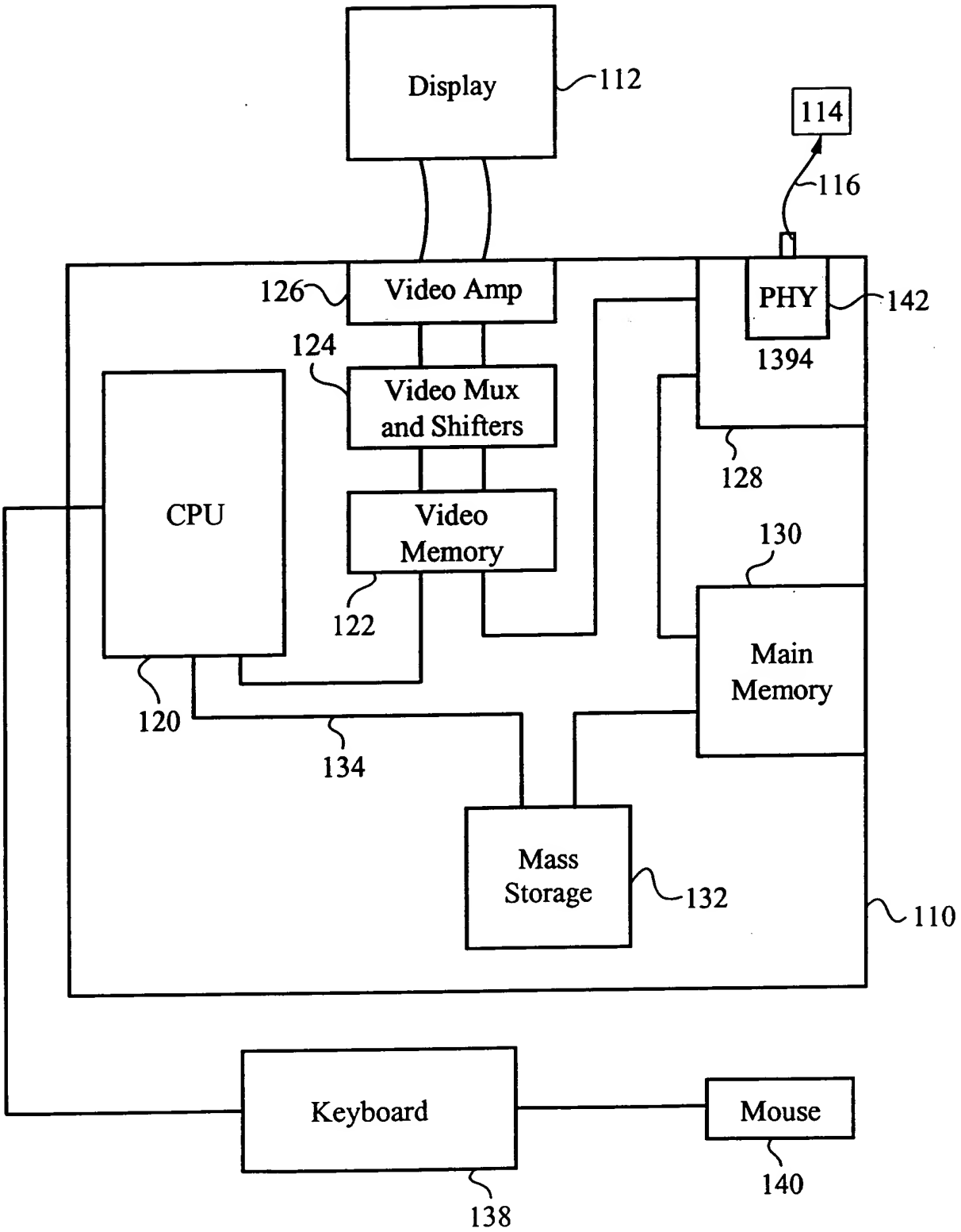
Display
112
114
116
126
Video Amp
PHY
142
1394
124
Video Mux
and Shifters
128
CPU
Video
Memory
130
122
Main
Memory
120
134
Mass
Storage
132
110
Keyboard
Mouse
140
138

Fig. 7

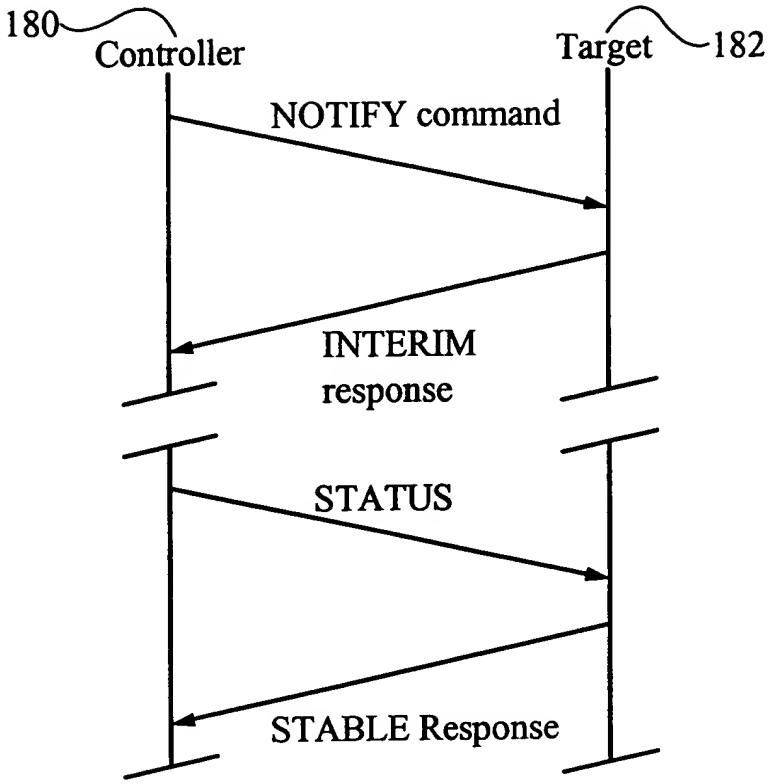
180
Controller
Target
182
NOTIFY command
INTERIM
response
STATUS
STABLE Response

Fig. 8

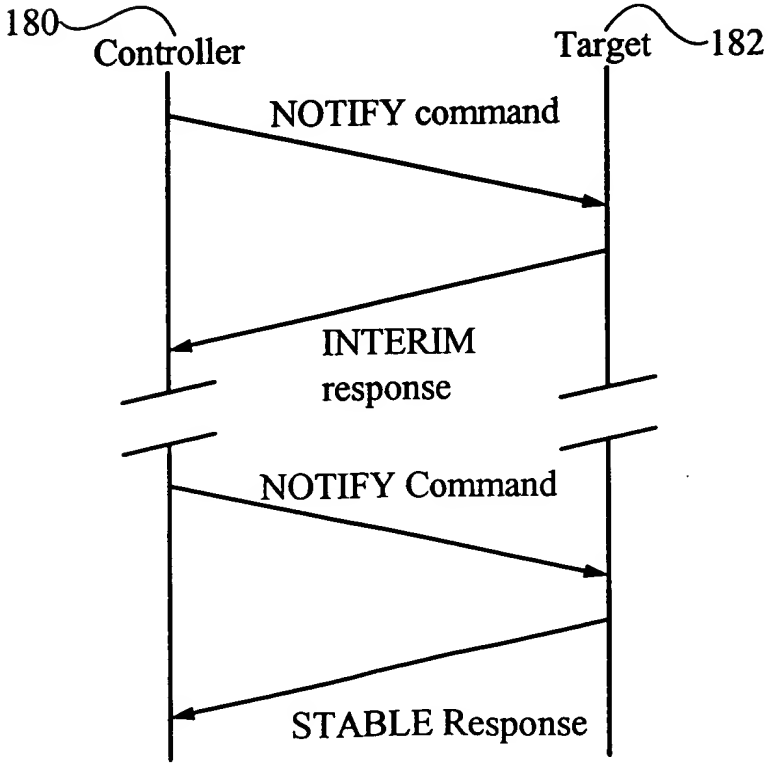
180
Controller
Target
182
NOTIFY command
INTERIM
response
NOTIFY Command
STABLE Response

Fig. 9

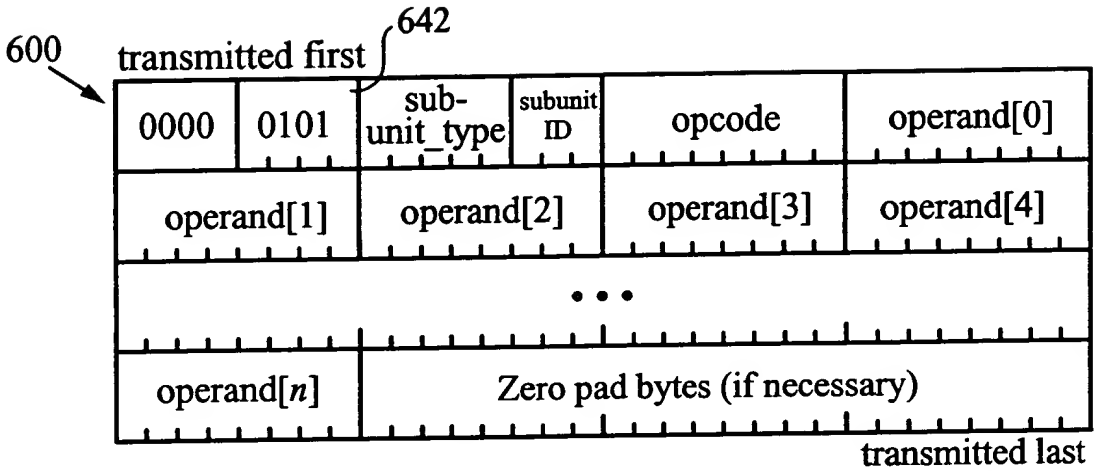
600
transmitted first
642
0000
0101
sub-unit_type
subunit ID
opcode
operand[0]
operand[1]
operand[2]
operand[3]
operand[4]
• • •
operand[n]
Zero pad bytes (if necessary)
transmitted last

Fig. 10

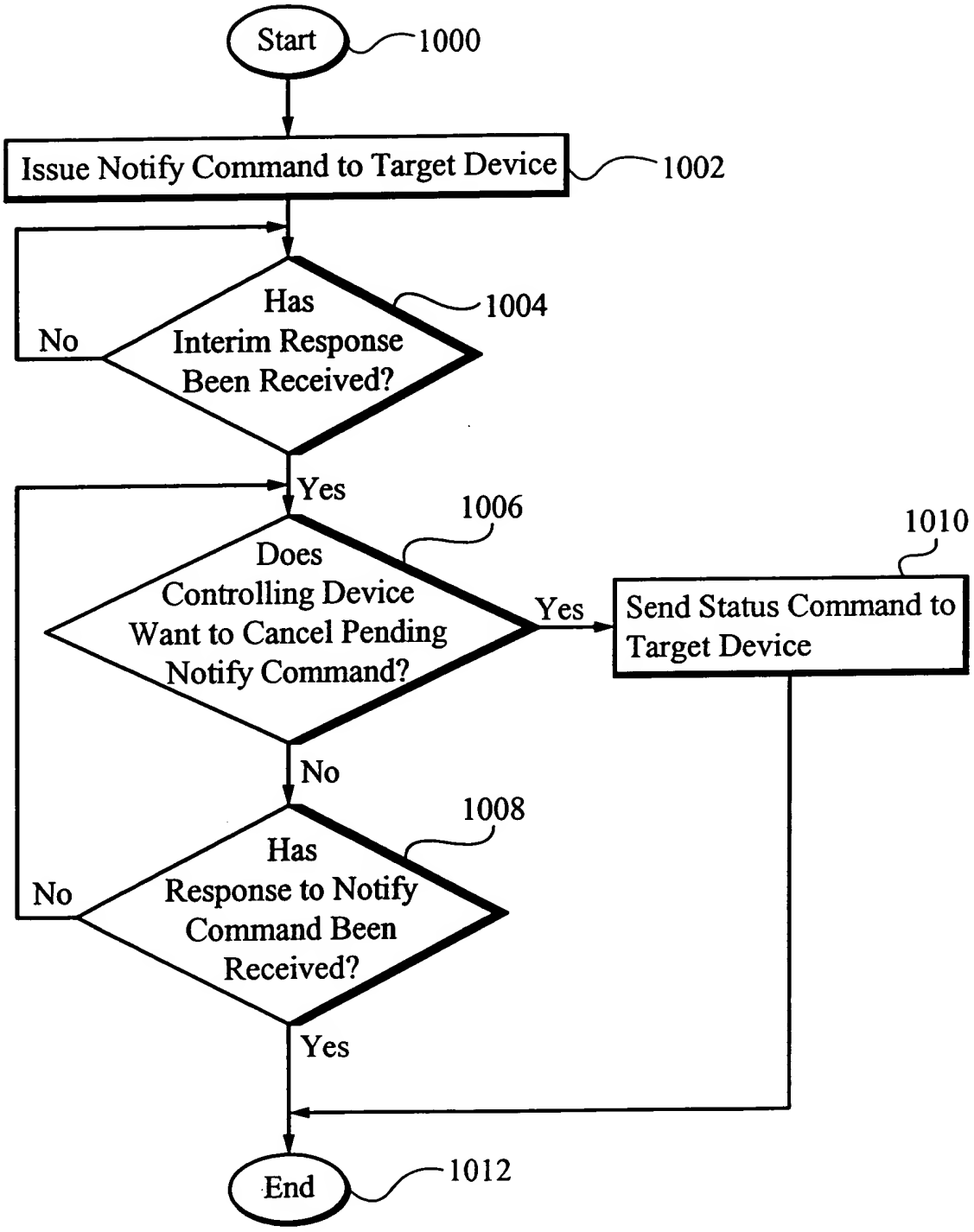
Start
1000
Issue Notify Command to Target Device
1002
Has Interim Response Been Received?
1004
No
Yes
Does Controlling Device Want to Cancel Pending Notify Command?
1006
Yes
Send Status Command to Target Device
1010
No
Has Response to Notify Command Been Received?
1008
No
Yes
End
1012

Fig. 11

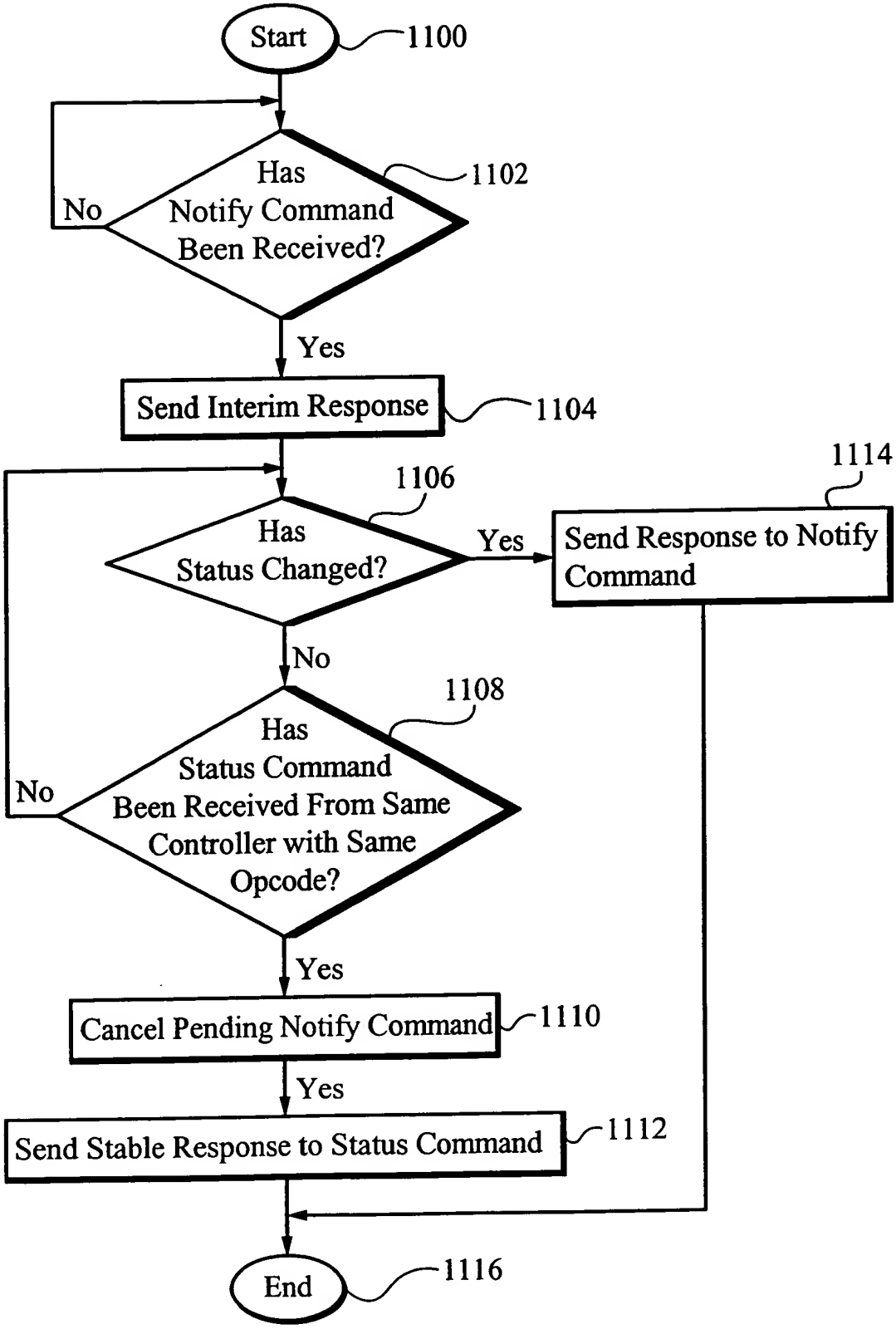
Start
1100
Has Notify Command Been Received?
1102
No
Yes
Send Interim Response
1104
1106
Has Status Changed?
Yes
1114
Send Response to Notify Command
No
1108
Has Status Command Been Received From Same Controller with Same Opcode?
No
Yes
Cancel Pending Notify Command
1110
Yes
Send Stable Response to Status Command
1112
End
1116

Fig. 12